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Date: June 10, 2005	Pages: 4 (including this sheet)
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REGARDING:

Title:	MEMORY ARRAY INCORPORATING MEMORY CELLS ARRANGED IN NAND STRINGS		
Application No.:	10/729,843	Filed:	December 5, 2003
Examiner:	Luu, Pho M.	Group Art Unit:	2824
Atty. Docket No.:	023-0031	Confirmation No.:	8321

ATTACHED HERETO:

- (1) Information Disclosure Statement 37 C.F.R. § 1.97(b) (2 pages)
- (2) Form PTO/SB/08A with copy of no references (1 page)

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Andrew C. Graham

Date

Respectfully submitted,



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Applicant(s): Luca G. Fasoli, et al.

Title: MEMORY ARRAY INCORPORATING MEMORY CELLS ARRANGED
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37 C.F.R. § 1.97(b)

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- ☒ Form(s) PTO/SB/08A (1 page(s), with copy of no reference(s)).
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Citation of such information shall not be construed as (i) an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention, (ii) a representation that a search has been made, other than as described herein, or (iii) an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

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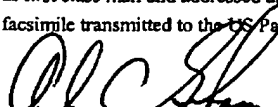
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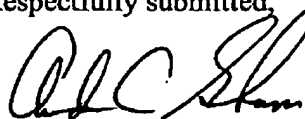
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- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
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Respectfully submitted,



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